

**SIPNA COLLEGE OF ENGINEERING AND TECHNOLOGY, AMRAVATI.**

**Department of Electronics & Telecommunication Engineering**

**Time Table - Odd Semester (Session 2019-20)**

**w.e.f. 17/ 06 /2019**

**YEAR/SEM:IV/VII - EXTC(A) Room No.:D-201**

Day/Time	10:30-11:30	11:30-12:30	12:30-1:00	1:00-2:00	2:00-3:00	3:00-3:15	3:15-4:15	4:15-5:15
Mon	DIP(ADG)	SOFC(AVM)	R E C E S S	VLSI(SSM)	SSJ(IMQC)	R E C E S S	PROJECT	
Tue	VLSI(SSM)	DIP(ADG)		SOFC(AVM)	FE-I(BSB)		PROJECT	LIB/SPORT
Wed	SOFC(AVM)	FE-I(BSB)		VLSI(SSM)/SDL-V(NTG)/INE-(A1/A2/A3)			INE/LIB	STUDENT ACTIVITY
Thu	DIP(ADG)	FE-I(BSB)		SSJ(IMQC)	VLSI(SSM)		VLSI(SSM)/SDL-V(AVM)/INE-(A2/A3/A1)	
Fri	VLSI(SSM)	SSJ(IMQC)		DIP(ADG)	SOFC(AVM)		VLSI(SSM)/SDL-V(NTG)/INE-(A3/A1/A2)	
Time	8:00 -9:00	9:00-10:00	10:00-10:15	10:15-11:15	11:15-12:15			
Sat	FE-I(BSB)	VLSI(SSM)		SSJ(IMQC)	PROJECT			

**YEAR/SEM:IV/VII - EXTC(B) Room No.:D-212**

Day/Time	10:30-11:30	11:30-12:30	12:30-1:00	1:00-2:00	2:00-3:00	3:00-3:15	3:15-4:15	4:15-5:15
Mon	VLSI(VTG)	SBM(IMQC)	R E C E S S	DIP(NTG)	SOFC(AAI)	R E C E S S	PROJECT	
Tue	DIP(NTG)	FE-I(SDJ)		VLSI(VTG)	SBM(IMQC)		VLSI(VTG)/SDL-V(AVM)/INE-(B2/B3/B1)	
Wed	SOFC(AAI)	VLSI(VTG)		SBM(IMQC)	FE-I(SDJ)		DIP(NTG)	STUDENT ACTIVITY
Thu	DIP(NTG)	SOFC(AAI)		FE-I(SDJ)	VLSI(VTG)		PROJECT	
Fri	VLSI(VTG)	FE-I(SDJ)		VLSI(VTG)/SDL-V(NTG)/INE-(B3/B1/B2)			PROJECT	LIB.
Time	8:00 -9:00	9:00-10:00	10:00-10:15	10:15-11:15	11:15-12:15			
Sat	VLSI(VTG)/SDL-V(NTG)/INE-(B1/B2/B3)			SOFC(AAI)	SBM(IMQC)			

VLSI LAB: Comp Lab 4

SDL-V LAB: Comp Lab 3

Time Table In-Charge  
Prof.S.D.JIRAPURE

HOD (ENTC)  
Dr.A.D.GAWANDE

