



Name of Faculty:

PROF. VIJAYA K. SHANDILYA

Professor & Head –Computer Science and Engineering

1.	Name in Full	Prof. Vijaya K. Shandilya	
2.	Mother's Name	Smt. Sushila K. Shandilya	
3.	Father's Name	Shri. Krishnakant Shandilya	
4.	Postal Address in Full	14 A, Dande Colony, Sai Nagar, Amravati 444607	
5.	Permanent Address	14 A, Dande Colony, Sai Nagar, Amravati 444607	
6.	E-mail Address	vkshandilya14@gmail.com	
7.	Designation	Professor	
8.	Department	Computer Science & Engineering	
9.	Date of Joining	14/12/2007	
10.	Total experience at Sipna COET	15.8 Years	
11.	Total Teaching/Professional Experience	Industrial Experience	-Nil-
		Teaching Experience	26
12.	Academic Qualification	M.E. PhD (C.S.E)	
13.	RESEARCH PUBLICATIONS: No. of Papers published in Journals	National	02
		International	65
	No. of Papers presented in Conferences /seminars	National	06
		International	11
14.	No. of Workshop /STTP/CEP/Training Programs Organized:	2/5/1/0	
15.	No. of Workshop /STTP/CEP/Training Programs Attended:	14/23/0/3	
16.	Membership of Professional Bodies/Societies: 1.CSI-(I1502518) 2.ISTE-(LM-29649) 3.IETE-(M-208183) 4.IE-(M-139190-3)		
17.	Any Other Information: PhD. Scholar registered are 06 and One Scholar completed his Ph.D. Patents: 05, Copyright: 02. Honour received as a project guide 6 times in State Level Project Exhibitions.		