Sipna College of Engineering and Technology

Department of Electronics and Telecommunication Engineering

Webinar

A Webinar on “RTL design functional verification ” was organized by Department of Electronics and Tele-communication Engineering on Dated 11th Oct.’2017. This Webinar was Presented by Mr. Sivakumar from Maven Silicon Banglore. This webinar described about the importance of RTL design & VLSI Approach , and also provides platform for international & Carrier Opportunities. The Event took place under guidance of Dr. A. P. Thakare, Head, Deptt. Of EXTC, Prof. V.V.Rathi, Student Activity in-charge and Prof. J. S. Edle were present