



Two weeks AICTE sponsored Faculty Development Programme on



“Digital Signal Processing and Design issues in VLSI of Image Processing”

26th September to 10th October 2017

Department of Electronics and Telecommunication Engineering, Sipna College of Engineering and Technology, Amravati has organized AICTE sponsored two week faculty development programme on “Digital Signal Processing and Design issues in VLSI of Image Processing” from 26th September to 10th October 2017. The FDP program received an overwhelming response with 60 participants from various institutes/colleges approved by AICTE and affiliated to various Universities across the arena of Maharashtra state.



Sr. No.	Name of Expert	Desination	Session Conducted	On Dated
1	Dr. Udhav Bhosale	Principal, RGIT, Mumbai	Keynote Address on Signals and Image Processing .	26/09/2017
2	Dr. A. Srinivasulu	R&D, Dean VFSTR, University Tamilnadu	Designing issues in VLSI	27/09/2017 to 28/04/2017
4	Dr. Nitin Kale	CTO, nanoscale tech., IIT Mumbai	Micro Fabrication	29/09/2017
5	Dr. V.R.Satpute	Asst. Prof. VNIT Nagpur	Fundamentals of DIP and Video Processing with Hands On	03/10/2017 to 05/10/2017
6	Mr. Avinash Mahalle	GCOE, Amravati	Hands on VLSI with Xilinx	06/10/2017
7	Dr. M.S.Sutaone	Professor, COEP, Pune	Wavelet Transform & Its Application	07/10/2017
8	Mr. Avinash Mahalle	GCOE, Amravati	Hands on VLSI with Xilinx	08/10/2017
9	MS. Vidya Viswanathan	Application Engineer, Mathswork Bangalore	Hands on Image processing based on FPGA on Matlab2017a	09/10/2017
10	Mr. Shrikant Atkarne	Application Engineer	Hands on Microwind	10/10/2017



Participants for the keynote on “Digital Signal Processing and Design issues in VLSI of Image Processing”



Date: 10th October, 2017

Valedictory function:

On the valedictory ceremony, Mr. S. R. Atkarne, Sr. Application Engineer, NI LOGIC PVT. LTD., Pune, join the session as a chief guest, along with Dr. A. P. Thakare, Head of Department Electronics Engineering, Dr. S. V. Rode and Prof. S. S. Mungona, coordinator of faculty development programme. On this occasion, Dr. A. P. Thakare, in his address, appreciates Dr. S V Rode and Prof. S. S. Mungona coordinator of faculty development programme, along with his team, for successful completion of the program.



Participant expressing feedback about faculty development program

Participants expressed their feedback and thanked the institution for the successful organisation of this two week faculty development program sponsored by AICTE, New Delhi. The participants also thanked for the delicious served food and comfortable lodging arranged by the institute .They expressed gratitude towards various experts from esteemed institutions. The lab sessions and the contents of the faculty development programme were proved beneficial for the researchers. The active participation of the participants in various lab sessions were felicitated.

At the end, the valedictory session was concluded with a vote of thanks given by, Dr. S V Rode, coordinator of faculty development programme, in which he has been paid his gratitude towards all the dignitaries who has spared their time and come from a far distance places to share their expertise with the participants. He has given thanks to sponsoring body of this faculty development programme with these words, “We are highly thankful to the AICTE, New Delhi, for providing financial assistance to organize this faculty development programme in Sipna college of engineering and technology, Amravati and hope we will find the support in future also for organizing such kind of activities.”